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7590

04/22/2004

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(Depositor's name) Haves (Signature) 2004 (Date)

APPLICATION NO. **FILING DATE** FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 09/751,551 12/28/2000 00 P 9119 US Jingyu Lian 9195

TITLE OF INVENTION: MULTI-LAYER PT ELECTRODE FOR DRAM AND FRAM WITH HIGH K DIELECTRIC MATERIALS

| APPLN. TYPE | SMALL ENTITY | ISSUB FEB | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|---|-------------------------------|-----------------|--|-----------------------|-----------------|
| nonprovisional | NO | \$1330 | \$300 | \$1630 | 07/22/2004 |
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| ORTIZ, E | DGARDO | 2815 | 257-310000 | | |
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| iniineon | rechno. | logies | AG |
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Munich. Germany

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July 14, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Lian, et al.

Docket No.:

00 P 9119 US

Serial No:

09/751,551

Art Unit:

2815

Date Filed:

December 28, 2000

TITLE:

Multi-Layer Pt Electrode For DRAM and FRAM with High K Dielectric

Materials

CERTIFICATION OF FACSIMILE TRANSMISSION

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